

03500.014352



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of:)
Masahiko YAMAGUCHI)
Appln. No.: 09/527,691)
Filed: March 17, 2000)
For: DATA PROCESSING APPARATUS)
AND METHOD FOR ENCRYPTION)
OR DECRYPTION OF)
COMMUNICATION DATA)

: Examiner: Taghi T. Arani
: Group Art Unit: 2131
: Patent No.: 6,959,088 B1
: Issued: October 25, 2005
: April 4, 2006

Certificate
APR 07 2006
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: CERTIFICATE OF CORRECTION BRANCH

REQUEST FOR CERTIFICATE OF CORRECTION
UNDER RULES 322 AND 323

Sir:

It is respectfully requested that a Certificate of Correction be issued by the Patent and Trademark Office due to errors which appear in the printed patent as a result of Patent and Trademark Office mistakes, and mistakes of a clerical, typographical, or minor character, which were not the fault of the Patent and Trademark Office. A Certificate of Correction form (Form PTO-1050), in duplicate, is enclosed.

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Accompanying this letter is a check for \$100.00 to cover the statutory fee for issuing a Certificate of Correction to correct errors of the patentee. Any deficiency should be charged, or any overpayment credited, to Deposit Account No. 06-1205. A duplicate of this paper is enclosed for this purpose.

Patentee's undersigned attorney may be reached by telephone in our Washington, D.C. office at (202) 530-1010 or by facsimile at (202) 530-1055. All correspondence should be directed to our address given below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Brian L. Klock', written over a horizontal line.

Brian L. Klock
Attorney for Patentee
Registration No. 36,570

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-3800
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,959,088 B1

DATED : October 25, 2005

INVENTOR(S) : Masahiko Yamaguchi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE DRAWINGS

Sheet 3, FIG. 3, "ANALYSE" should read --ANALYZE--.

Sheet 4, FIG. 4, "ANALYSE" should read --ANALYZE--.

Sheet 7, FIG. 7, "ANALYSE" should read --ANALYZE--.

Sheet 10, FIG. 10, "ANALYSE" should read --ANALYZE--.

Sheet 11, FIG. 11, "ANALYSING" should read --ANALYZING--.

Sheet 12, FIG. 12, "ANALYSE" should read --ANALYZE--.

COLUMN 6

Line 4, "form" should read --from--.

COLUMN 7

Line 10, "a" should be deleted.

Line 29, "a" should be deleted.

COLUMN 8

Line 17, "a" should read --at--.

Line 47, "step;" should read --step, wherein the predetermined bit positions are a predetermined number of upper bit positions;--.

Line 51, "step," should read --step.--.

Lines 52 and 53 should be deleted.

MAILING ADDRESS OF SENDER:

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